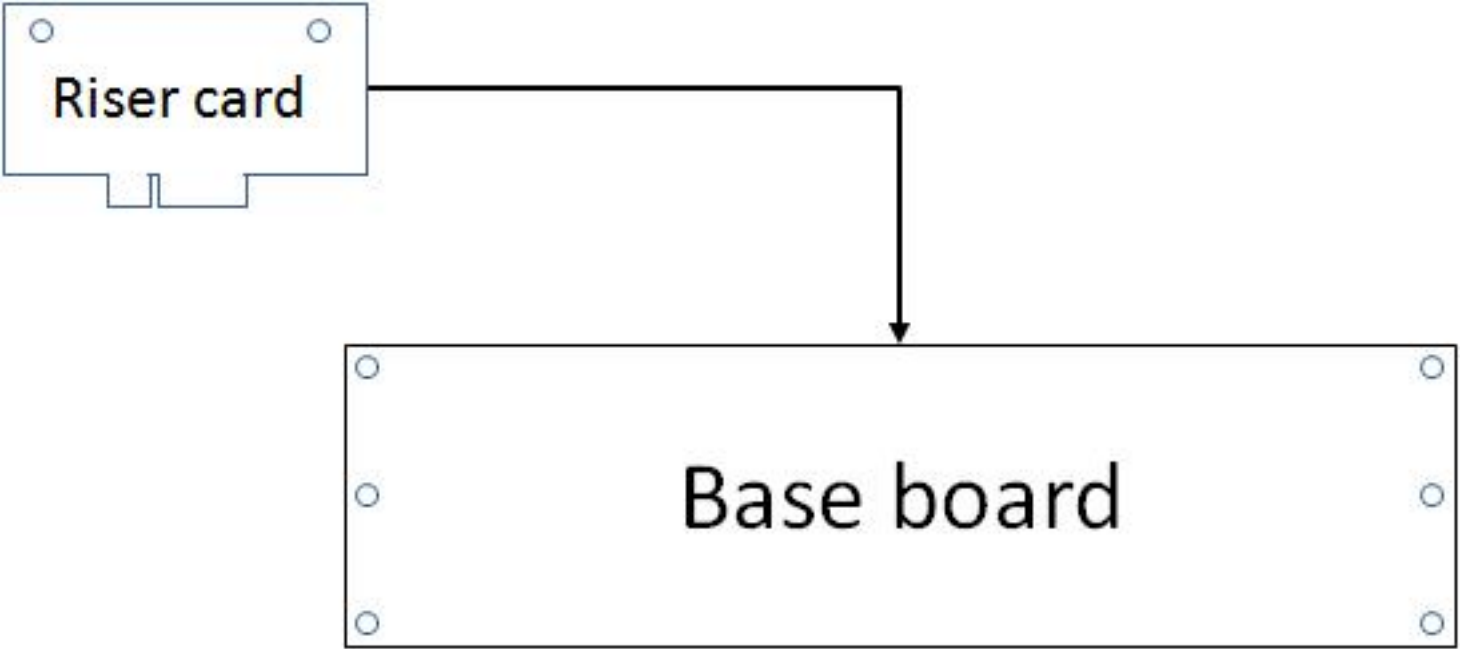
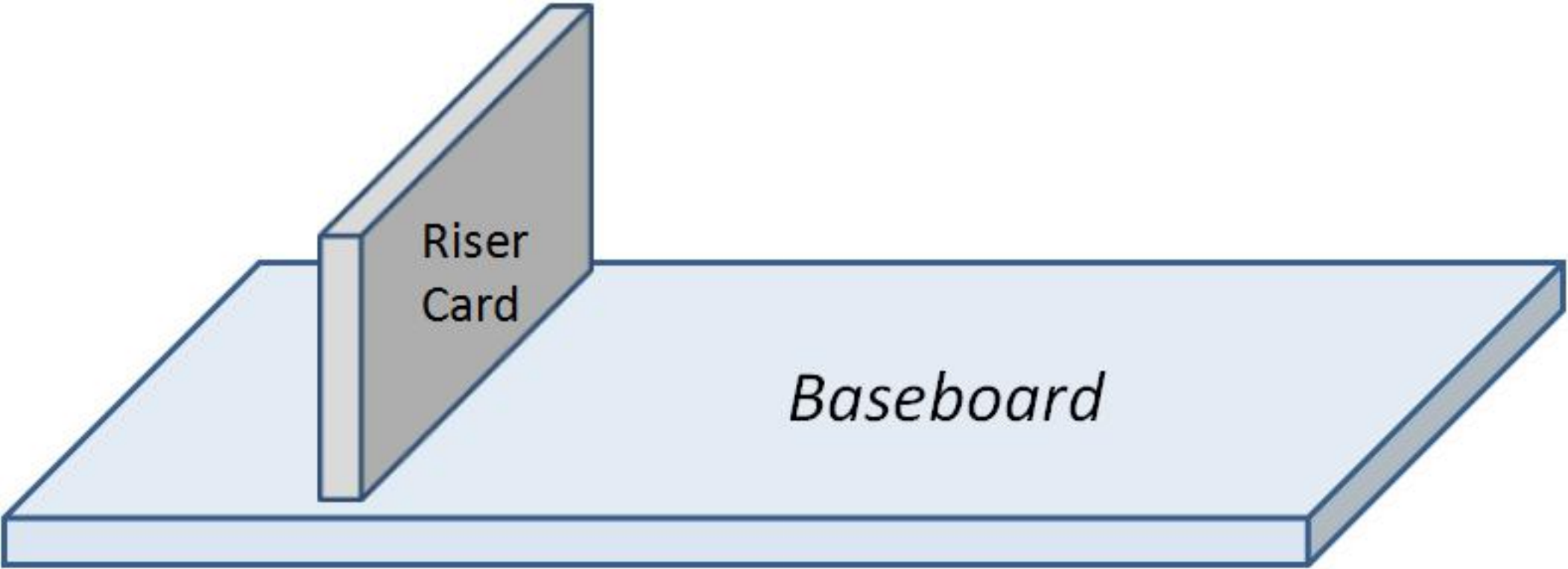


4		3		2		1		
INTEL CONFIDENTIAL				REVISION HISTORY				
				ZONE	REV	DESCRIPTION	DATE	APPROVED
<div>HILLE GREEN FAB 3</div> <div>CONNECTOR</div> <div>PCIE GEN 3 TEST BOARD</div> <div>FAB 3</div> <div>REVISED 10/07/2011</div>								
<div>BASE BOARD</div> <div>G45494-301 PBA,MB,PCIE GEN3 BB,FAB3</div> <div>G45493-003 PB,EMSD,PCIE GEN3 BB,FAB3</div> <div>G45487 PB,DOC,EMSD,PCIE G3,FAB3</div> <div>G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3</div> <div>RISER CARD</div> <div>G45492-301 PBA,RC,PCIE GEN3 RC,PCIE G3 CEB RC,FAB3</div> <div>G45491-003 PB,EMSD,PCIE G3 CEB RC,FAB3 (NEW)</div> <div>G45487 PB,DOC,EMSD,PCIE G3,FAB3 (NEW)</div> <div>G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3</div>				Fri Oct 07 16:48:40 2011				
INTEL CONFIDENTIAL				DESIGNED BY	DATE	Intel Corporation2200 Mission Collage Blvd. P.O. BOX 58119 Santa Clara, CA 95052-8119		
				DRAWN BY	DATE			
				CHECKED BY	DATE	TITLE HILLE_GREEN		
				APPROVED BY	DATE			
ESE		SIZE	CODE	DOCUMENT NUMBER	REV			
		B	34649	G47418	0.1			
		SCALE: NONE		DO NOT SCALE DRAWING		SHEET 1 OF 6		
4		3		2		1		

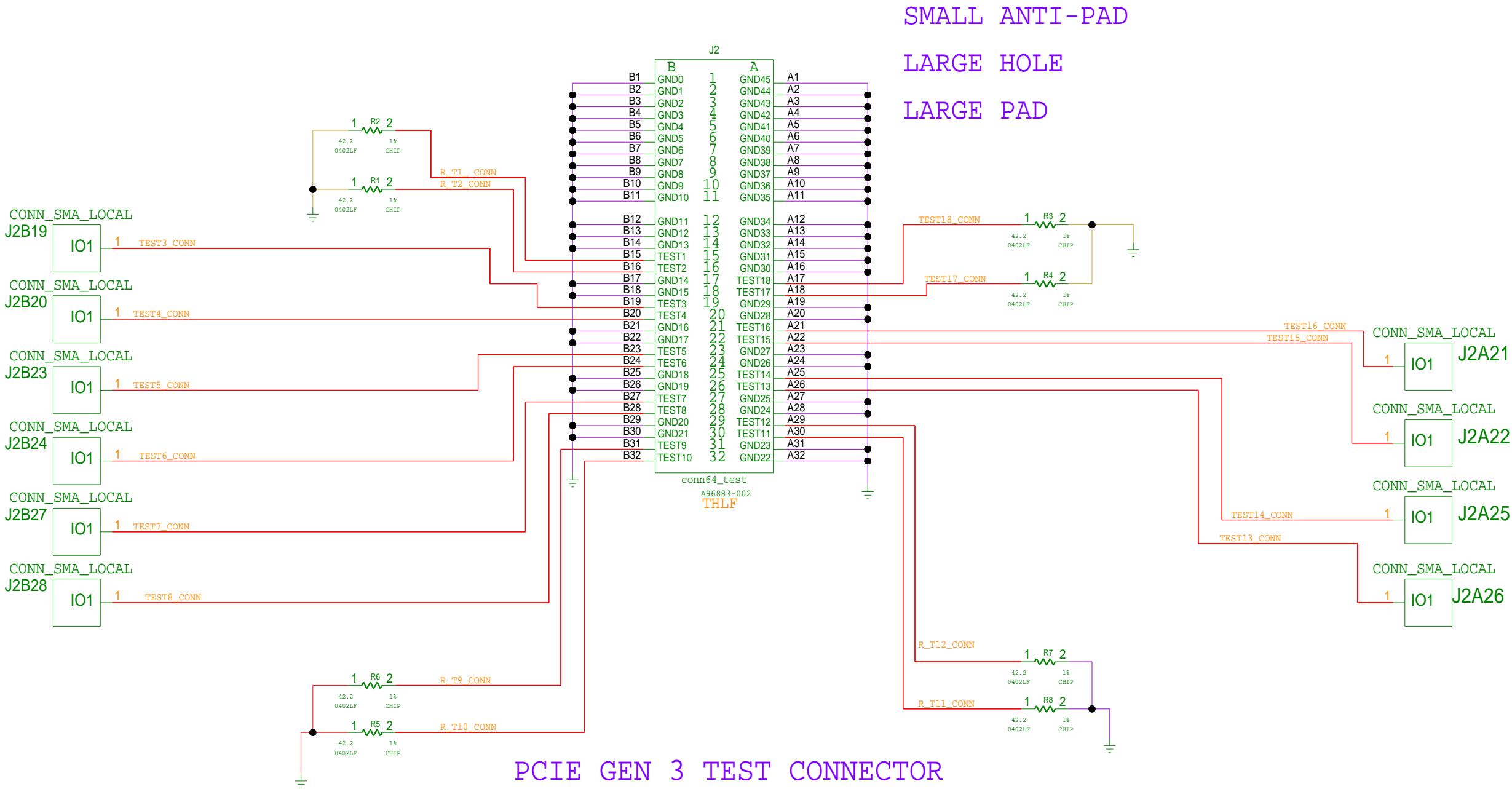
RISER CARD & BASE BOARD
BLOCK DIAGRAM
VIEW 1



RISER CARD & BASE BOARD
BLOCK DIAGRAM
VIEW 2



BASE BOARD



DRAWING
TITLE=PCIE_GEN3_CONN
ABBREV=PCIE_GEN3_CONN
LAST_MODIFIED=Fri Oct 07 16:14:48 2011

Fri Oct 07 16:14:48 2011

BASE BOARD

INTEL CONFIDENTIAL

DEPARTMENT
ESE

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2200 Mission College Blvd.
P.O. BOX 58119
Santa Clara, CA 95052-8119

SIZE
B

CODE
34649

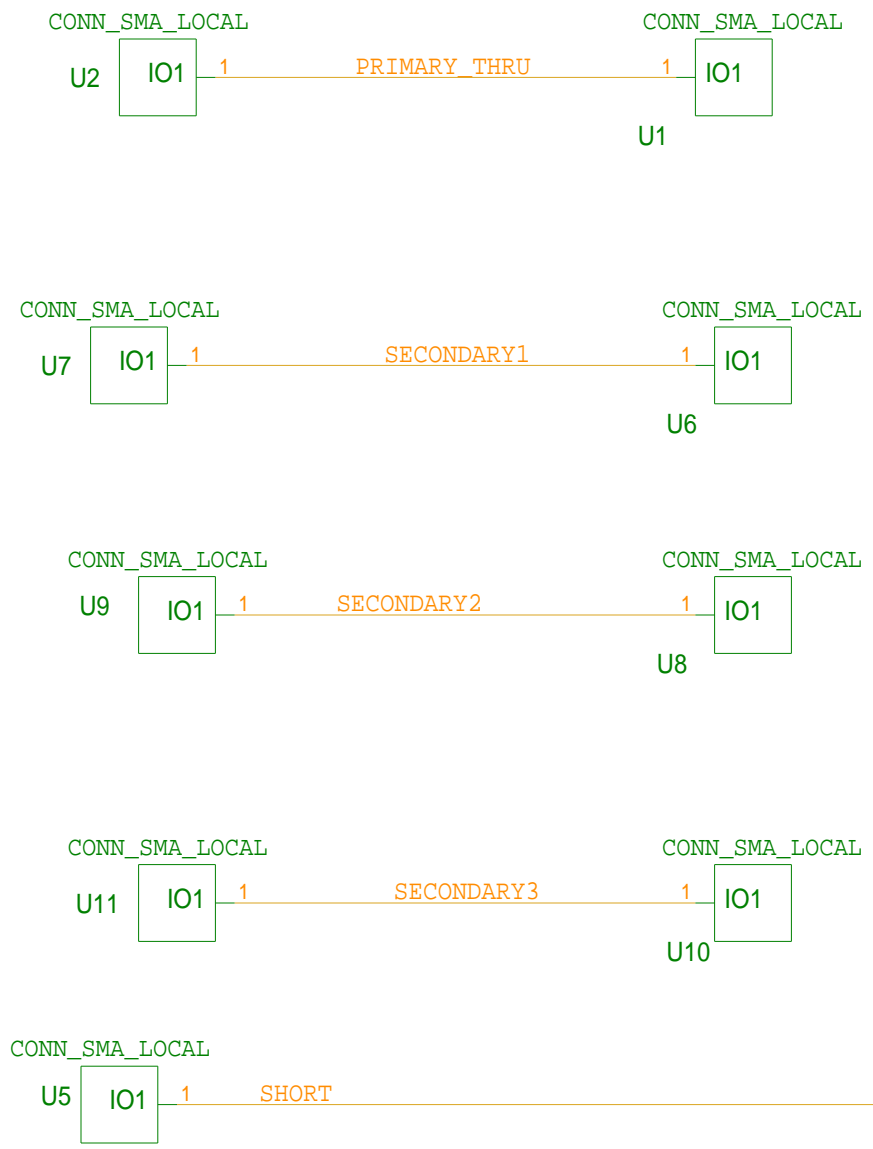
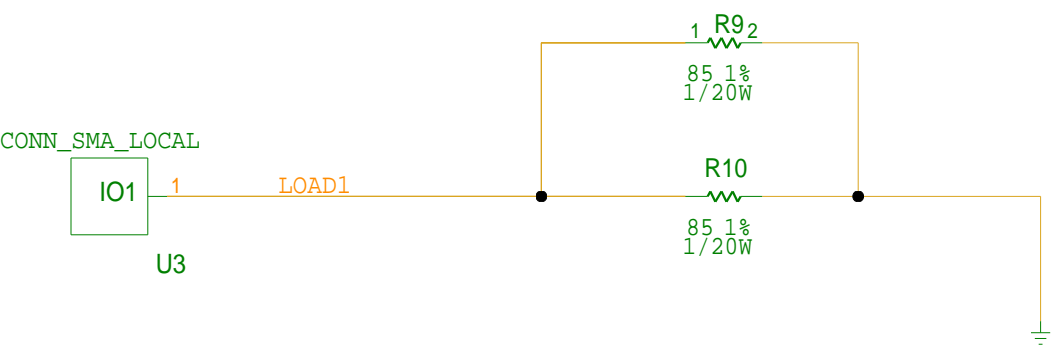
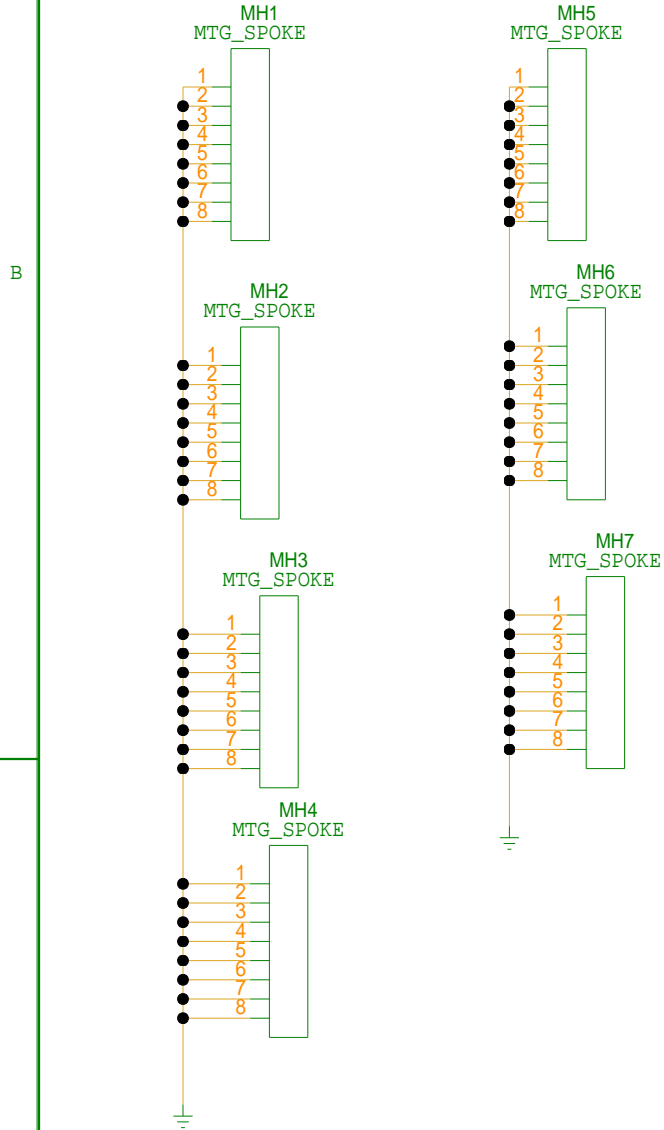
DOCUMENT NUMBER
G47418

REV
0.1

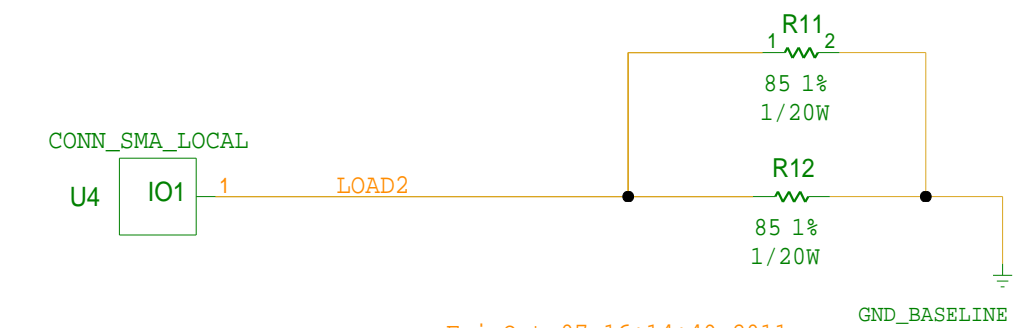
SCALE: DO NOT SCALE DRAWING SHEET 3 OF 6

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TRL CALIBRATION STRUCTURES - SMA
BASE BOARD BOTTOM SIDE



THIS SIGNAL IS
SHORTED TO GND
AT 1730 MILS



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TRL CALIBRATIONS

DEPARTMENT	Intel Corporation	SIZE	CODE	DOCUMENT NUMBER	REV
ESE	2200 Mission College Blvd. P.O. BOX 58119 Santa Clara, CA 95052-8119	B	34649	G47418	0.1
SCALE:		DO NOT SCALE DRAWING			SHEET 4 OF 6

RISER CARD SCHEMATIC

RISER CARD
G45492-301 PBA,RC,PCIE GEN3 RC,PCIE G3 CEB RC,FAB3
G45491-003 PB,EMSD,PCIE G3 CEB RC,FAB3 (NEW)
G45487 PB,DOC,EMSD,PCIE G3,FAB3 (NEW)
G47418 PB, SCHEMATIC, EMSD, PCIE G3, FAB 3

RISER CARD

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B

CODE
34649

DOCUMENT NUMBER
G47418

REV
0.1

SCALE:

DO NOT SCALE DRAWING

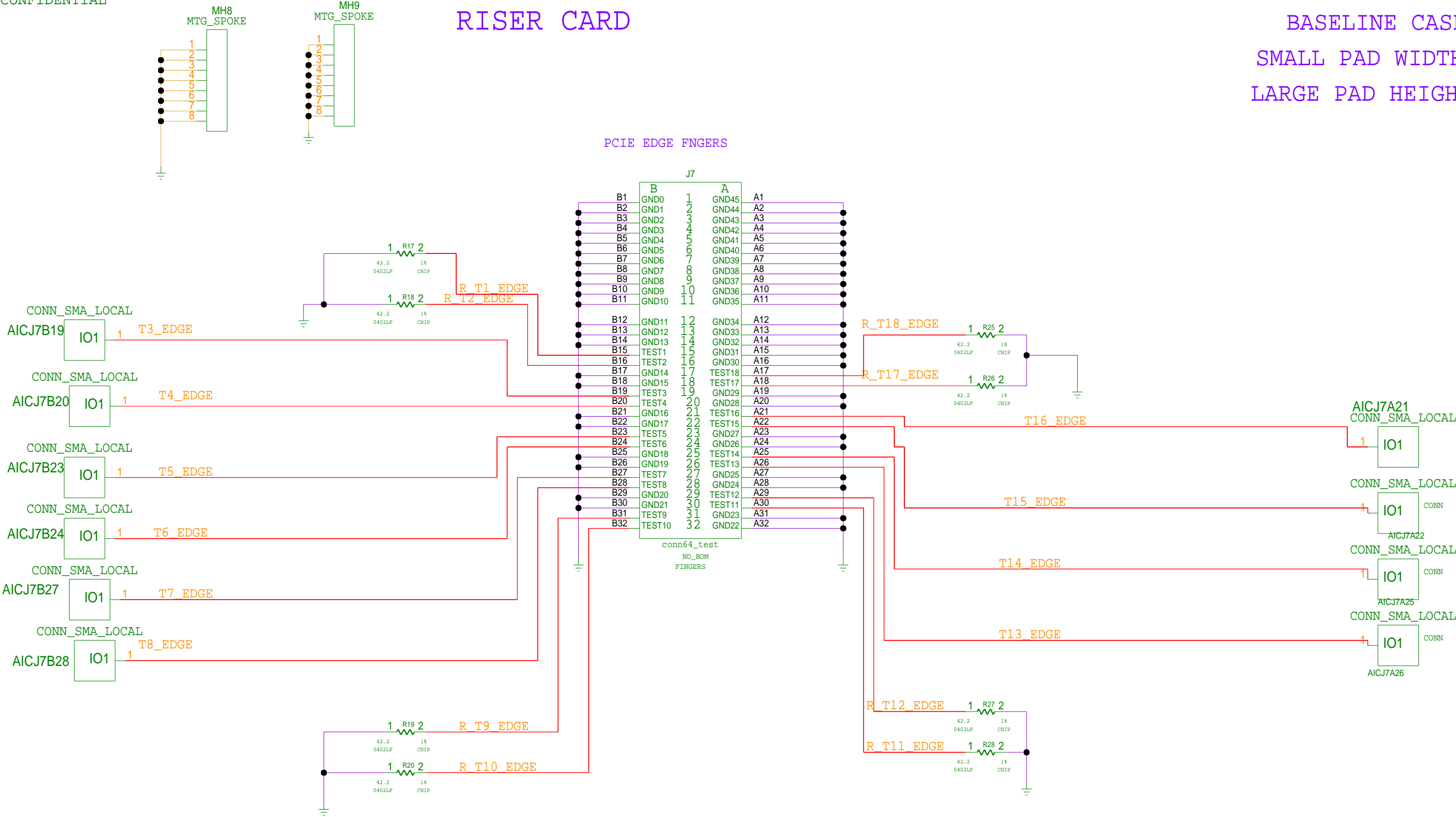
SHEET 5 OF 6

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RISER CARD

BASELINE CASE
SMALL PAD WIDTH
LARGE PAD HEIGHT



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RISER CARD

DRAWING
TITLE=PCIE_GEN2_CONN
ABBREV=PCIE_GEN2_CONN
LAST_MODIFIED=Fri Oct 07 16:14:48 2011

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CODE
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DOCUMENT NUMBER
G47418

REV
0.1

SCALE:

DO NOT SCALE DRAWING

SHEET 6 OF 6

Fri Oct 07 16:14:48 2011¹