



**PROCESS CHANGE NOTIFICATION
FLEX 10K AND FLEX 6000 ADDITIONAL SOURCE OF SUPPLY**

Altera is adding WaferTech as an additional source of supply for FLEX 10K and FLEX 6000 products currently run at TSMC. WaferTech is a joint venture with TSMC, Altera and other partners to manufacture wafers in a state-of-the-art facility in Camas, Washington. Altera has worked closely with TSMC in transferring both the design rules and process to the WaferTech facility. WaferTech has successfully replicated TSMC's 0.35-micron process. The two processes are identical, and a full qualification is in progress.

This PCN is to notify you of Altera's intent to meet future demand by supplying product from either of the two fabrication lines. This change improves Altera's ability to support the increasing demand for FLEX 10K and FLEX 6000 products.

A schedule for the inclusion of WaferTech is shown below:

Device	Inclusion Date**
EPF10K50V	July 31, 1998
EPF10K100A	July 31, 1998

The WaferTech process may be distinguished by the second (β), fourth and fifth ($\alpha\alpha$) digit characters of either the Altera lot number marked on the backside, or the date code marked on top of the device.

Backside Lot Number	Topside Date Code
L β Z $\alpha\alpha$ #####	X β Z $\alpha\alpha$ YYWW

Device	β	$\alpha\alpha$	Layers of metal	Lot Number Examples	Date Code Example
EPF10K50V	C	42	3	LCz42#####	XCz42YYWW
EPF10K100A	C	43	4	LCz43#####	XCz43YYWW

Other FLEX 10K and FLEX 6000 devices are planned to be added to WaferTech in Q4, 1998.

If you have any questions on this update, please contact your local Altera sales representative.

** Qualifications are in process and reports can be made available upon request. Please contact Altera's Customer Quality Engineering Manager, Paul Weis, at (408) 544-7563 for more details.