



PROCESS CHANGE NOTIFICATION

PCN1907

Alternate Wafer Fabrication Site for Stratix®10 Devices

Change Description:

Intel Programmable Solutions Group (“Intel PSG”, formerly Altera) is announcing the addition of the Intel Fab 24 site in Leixlip, Ireland as an alternate wafer fabrication site of Stratix 10 devices.

Intel Fab 24 is an ISO 9001:2015 certified facility that has been in production for 15 years, and in high volume fabrication of the 14nm process technology for >3 years.

Table 1

	Current Site	Added Alternate Site
Wafer Fabrication Site	Intel Oregon, USA (D1C)	Intel Leixlip, Ireland (Fab 24)

Products Affected:

All Stratix 10 devices currently in production and all future Stratix 10 devices.

Please refer to the list of currently assembled and tested Stratix 10 devices in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/pcn1907-opn-list.xlsx>

Recommended Action

Customers are requested to:

1. Acknowledge receipt of this notification.
2. Review and inform us, at the earliest convenience, of any questions or concerns regarding this change.

Please refer to the “Product Transition Dates” for the key milestones.

Product Transition Dates:

Customers are requested to take note of the key dates shown in the table below.

Table 2

<i>Milestone</i>	<i>Date</i>
Last date to acknowledge receipt of this notification ¹	July 26, 2019
Estimated earliest shipment date of changed products ²	September 1, 2019

Note 1: J-STD-046, section 3.2.3.1b, stipulates that lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.

Note 2: Effective the earliest ship date listed above, Intel PSG may begin the shipment of changed products.

Intel reserves the right to continue shipment of pre-change product after the change implementation date, and customers will receive shipments of either pre-change or post-change product.

Reason for Change:

The qualification of an additional wafer fabrication site for the affected devices supports supply chain risk mitigation.

Impact and Benefit of Change:

The change will not impact the form, fit, and function of the product. Product datasheet and package specifications remain the same.

Qualification of Change:

Qualification will be performed according to Intel's Copy Exactly! (CE!) Methodology to ensure consistency in quality and reliability of the product and processes.

Contact

For more information, please contact Sales or Customer Quality Engineering (CQE) in your region, or submit a Service Request at the [My Intel](#) support page.

Customer Notifications Subscription

Customers that have subscribed to Intel PSG's customer notification mailing list will receive the PCN document automatically via email.

If you would like to receive customer notifications by email, please subscribe to our customer notification mailing list at:

<https://www.intel.com/content/www/us/en/programmable/my-intel/mail-emailsub/technical-updates.html>

Intel PSG references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.

Revision History

Date	Rev	Description
05/03/2019	1.0.0	Initial Release

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