



CUSTOMER ADVISORY

ADV1828

Readiness Plan: EU RoHS Exemption 15 Scope Change

Change Description:

Intel Programmable Solutions Group ("Intel PSG", formerly Altera) is notifying customers of the readiness plan addressing the EU Restriction of Hazardous Substances (RoHS) Directive Exemption 15 scope change.

Exemption 15 scope for electronic products originally covered:

Lead in solders to complete a viable electrical connection between semiconductor die and carrier within integrated circuit flip chip packages

Upon revision, the scope of Exemption 15a will only cover:

Lead in solders to complete a viable electrical connection between the semiconductor die and carrier within integrated circuit flip chip packages where at least one of the following criteria applies:

- a semiconductor technology node of 90 nm or larger;
- a single die of 300 mm² or larger in any semiconductor technology node;
- stacked die packages with die of 300 mm² or larger, or silicon interposers of 300 mm² or larger.

Changes will affect Field Programmable Grid Array (FPGA) Flip Chip products that currently use leaded solder bumps on the 1st level interconnect.

- Selected products will be converted to the leadfree SnAg1.8 solder bumps, replacing the 63/37 Sn-Pb bumps.
- There will also be a selected set of products that will not transition to leadfree solder bumps and will eventually be discontinued.

See Table 1 for the conversion plan and Table 2 for important information about the converted products.

Table 1: Leadfree Solder Bumps Conversion Plan

Conversion Timeline	Converting in 2019 <i>Immediately impacted with latest Ex15 scope change</i>	Converting in 2020 <i>Applicable Ex15a extension to year 2021</i>	Not Converting
Product Family	<ul style="list-style-type: none"> • Arria II GX • Arria V (small die) • Stratix III (small die) • Stratix IV (small die) 	<ul style="list-style-type: none"> • Arria II GZ • Arria V (large die) • Stratix II • Stratix III (large die) • Stratix IV (large die) • Stratix V 	<ul style="list-style-type: none"> • Arria GX • HardCopy II • HardCopy III • HardCopy IV • Hardcopy Si Pro
Samples and Production Timeline	Converted samples can be ordered now.	Converted samples will be available in 2020.	No available converted parts.

Note: Small die is < 300mm²; Large die is ≥ 300mm²

Table 2: Important Information About The Converted Products

Ordering Code	<p>New Ordering Part Numbers (OPNs) with the “G” suffix will be assigned to converted products.</p> <p>Existing OPNs with leaded solder bumps will eventually be retired and a separate Product Discontinuation Notice (PDN) will be issued to officially notify customers.</p>
Quality and Reliability Qualification Status	<p><u>Products for year 2019 conversion:</u> Package reliability qualification testing has been successfully completed.</p> <p><u>Products for year 2020 conversion:</u> Package reliability qualification is in progress and targeted for completion by end of 2019.</p>

Table 2 (continued): Important Information About The Converted Products

Assembly Site	The assembly sites are the same: ASE Taiwan and Amkor Korea.
Bill of Materials	The 63/37 Sn-Pb leaded bumps will be replaced with leadfree SnAg1.8 bumps. The leadfree solder bumps necessitated changes to other materials within the BOM (Bill of Materials) to meet manufacturability and package reliability requirements.
Package Form and Fit	There will be modifications to the package form and fit for the following product families: Stratix III & Arria II GX (lidless package). Applicable Package Outline Drawings (POD) for the converted products are available upon request.
Function and Electrical Specification	The change does not impact the function and electrical specifications of the affected products.

Recommended Action:

It is recommended for customers to switch to the fully lead-free converted products (with the “G” suffix). Please see the link provided in the “Products Affected” section where the new OPNs of the converted parts are listed.

Products Affected:

The link below contains the list of affected OPNs with the corresponding conversion status and new OPN (where applicable).

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv1828-opn-list.xlsx>

Reason for Change:

This change is in preparation for the RoHS Directive Exemption 15 scope change. This change also aligns with Intel’s long-standing commitment to preserving the environment, health, and safety.

Contact

For more information, please contact your Sales representative or submit a Service Request at the [My Intel](#) support page.

Revision History

Date	Rev	Description
01/18/2019	1.0.0	Initial Release

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